

FIG. 1A

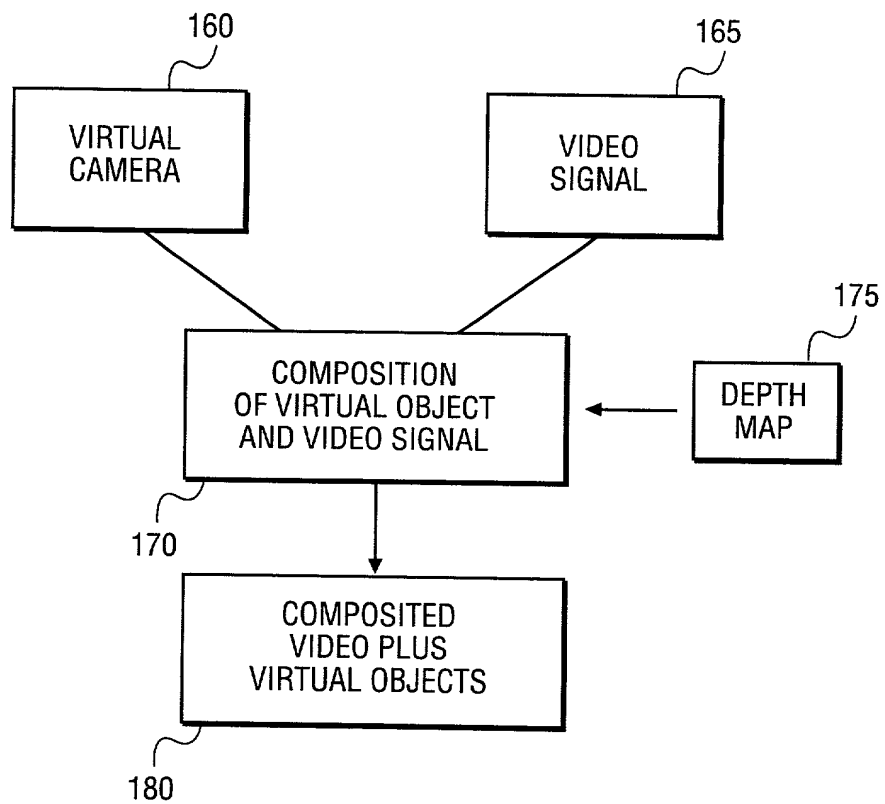


FIG. 1B

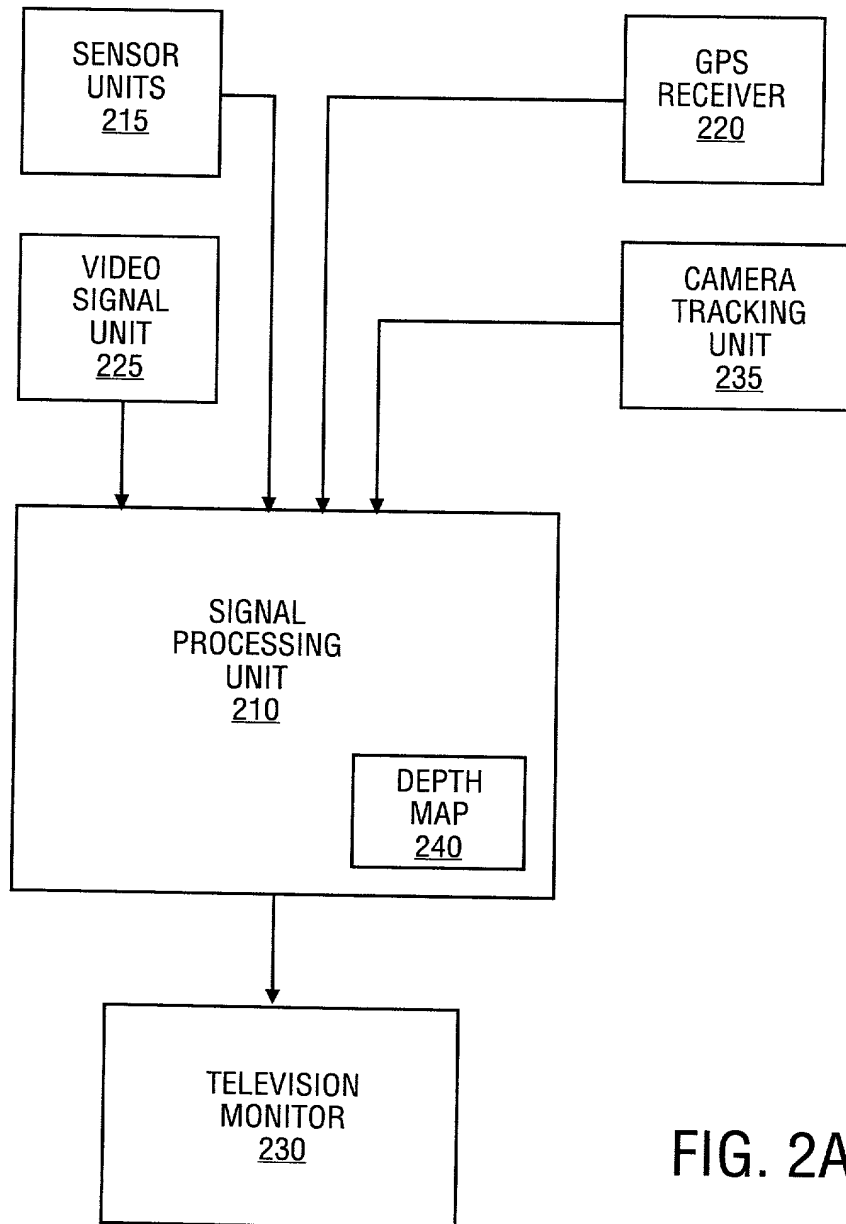


FIG. 2A

FIG. 2B is a block diagram of a system 250. The system 250 includes a main memory 258, a static memory 260, a bus 252, a CPU 254, and a processor 256. The CPU 254 includes a control unit 262, an ALU 266, and registers 268. The main memory 258 and static memory 260 are connected to the bus 252. The bus 252 is connected to the CPU 254 and the processor 256.

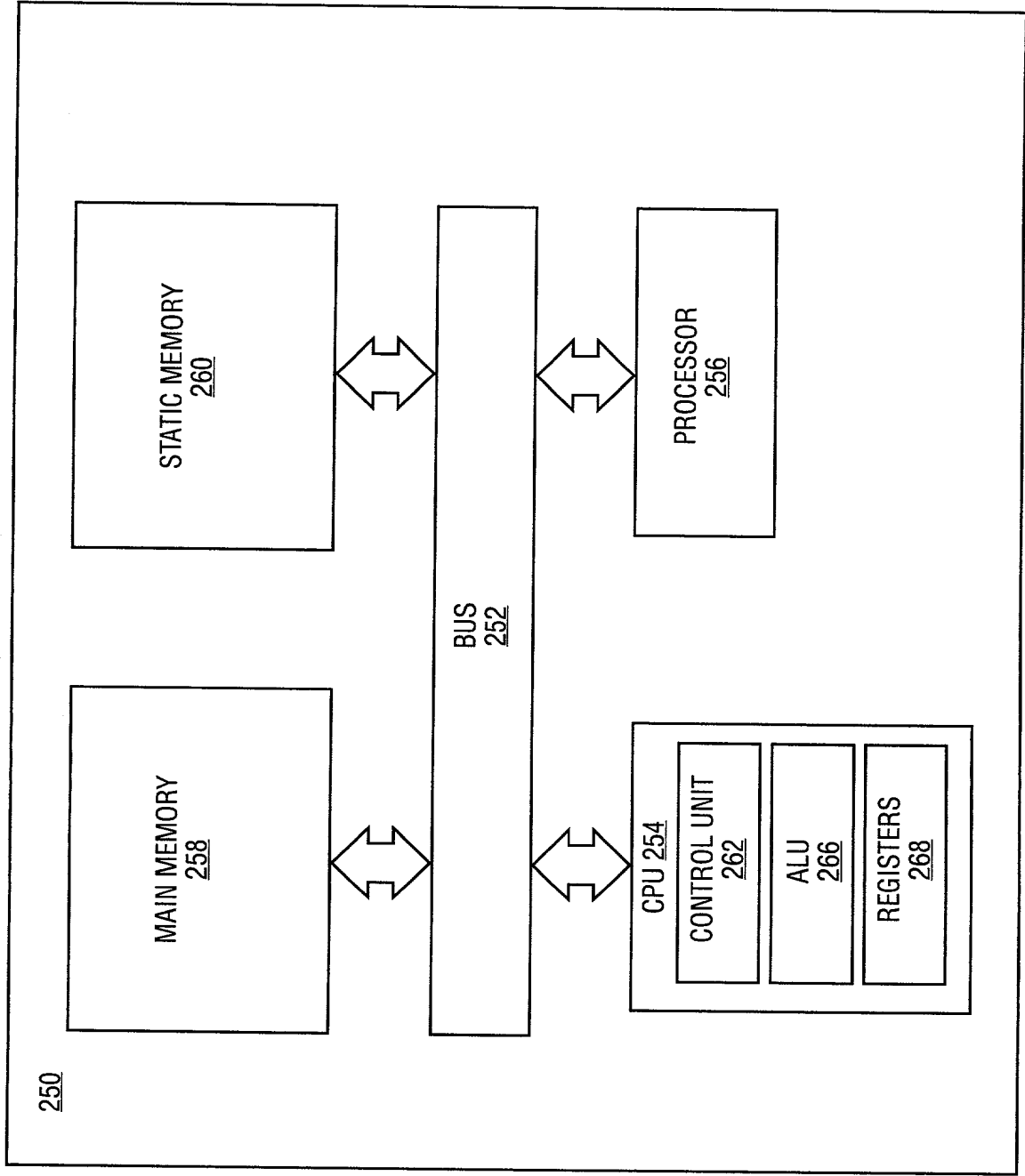


FIG. 2B

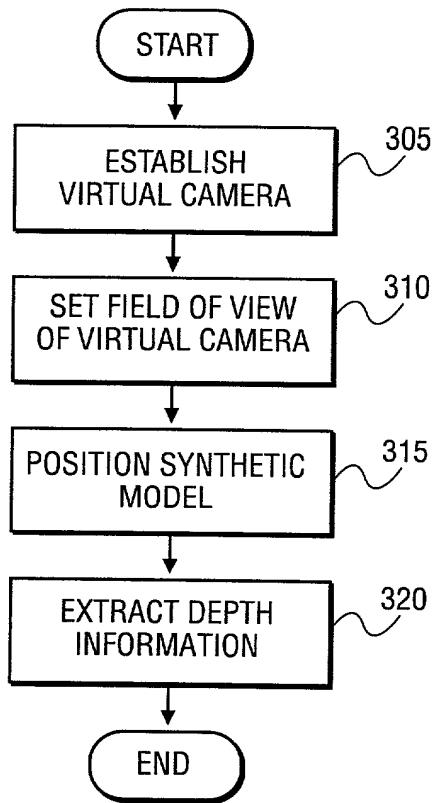


FIG. 3A

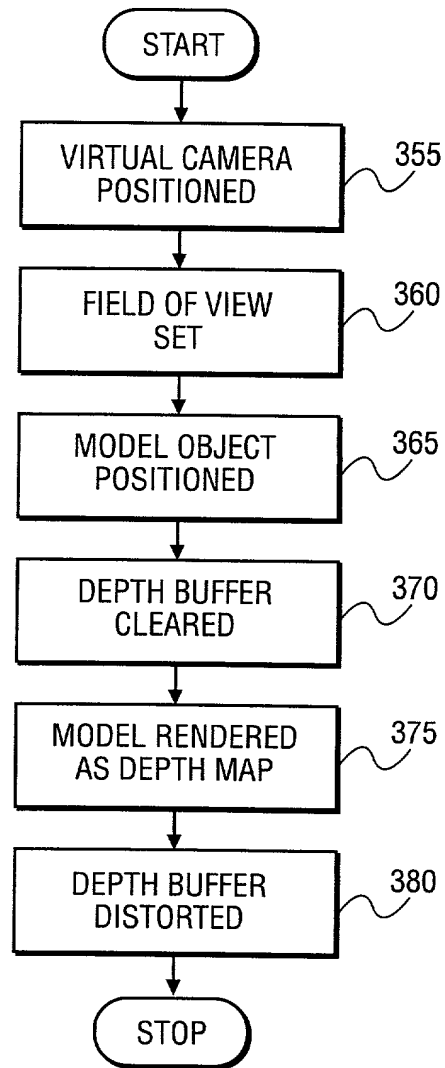


FIG. 3B

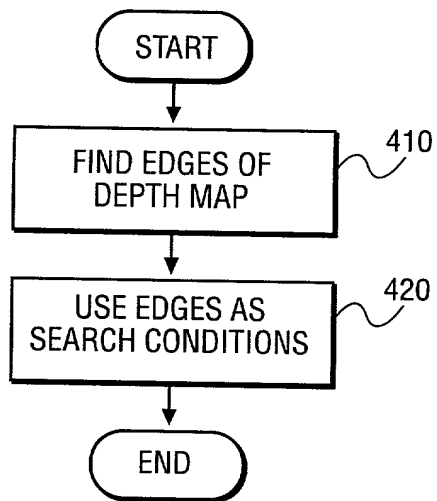


FIG. 4

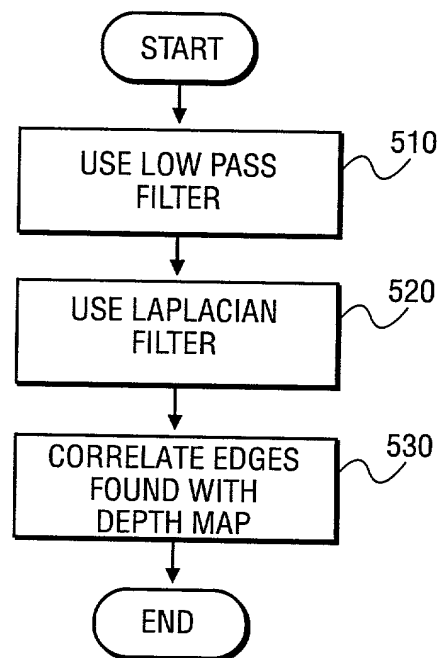


FIG. 5